

Electronic Patent Application Fee Transmittal

Application Number:	10748906			
Filing Date:	30-Dec-2003			
Title of Invention:	SEMICONDUCTOR MEMORY DEVICE TESTABLE WITH A SINGLE DATA RATE AND/OR DUAL DATA RATE PATTERN IN A MERGED DATA INPUT/OUTPUT PIN TEST MODE			
First Named Inventor/Applicant Name:	Jong-Cheol Lee			
Filer:	Frank Chau/marg dougan			
Attorney Docket Number:	8836-223JHM/ID12244US			
Filed as Large Entity				
Utility under 35 USC 111(a) Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Utility Appl issue fee	1501	1	1510	1510
Publ. Fee- early, voluntary, or normal	1504	1	300	300

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension-of-Time:				
Miscellaneous:				
Printed copy of patent - no color	8001	2	3	6
Total in USD (\$)				1816